

INFORMATION DISCLOSURE CITATION				ATTY. DOCKET NO. P208-C1/WLP		SERIAL NO. not assigned			
PTO-1449				APPLICANT Srinivasan et al					
				FILING DATE 2/9/2004		GROUP 2824			
U.S. PATENT DOCUMENTS									
EXAMINER'S INITIALS		PATENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE		
FOREIGN PATENT DOCUMENTS									
EXAMINER'S INITIALS		PATENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	Translation		
							Yes	No	
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)									
HN		U.S. Patent Application Serial No. 10/143,051 entitled Content Addressable Memory Having Column Redundancy and filed on 5/10/2002. (now US Patent 6,714,430)							
EXAMINER				DATE CONSIDERED					
HIEN NGUYEN				7/10/04					

**EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.